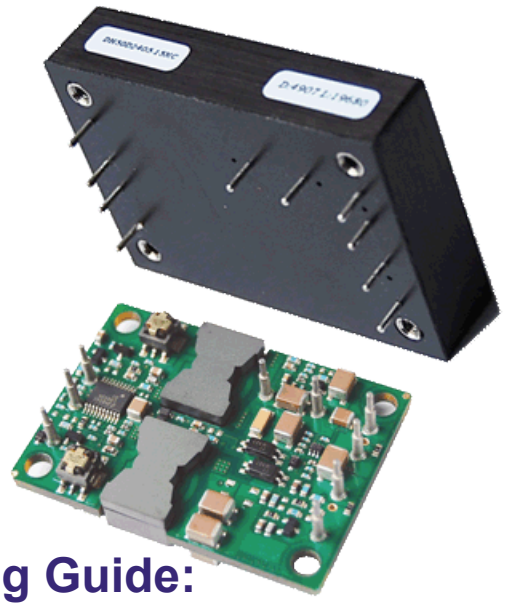


FEATURES:

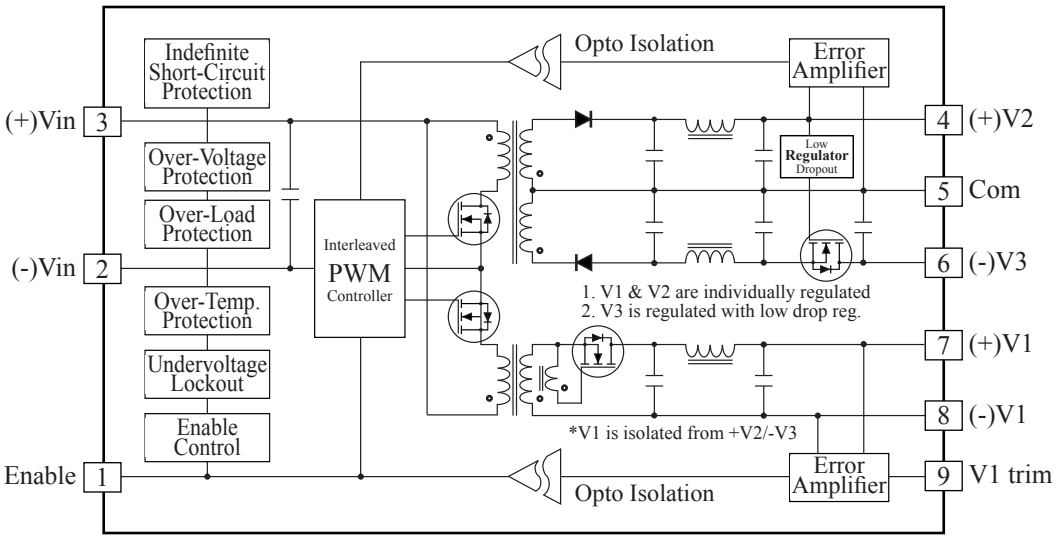
- All outputs are regulated
- High reliability planar xmer design
- Ultra low ripple & noise
- No external capacitor required
- High power density
- High effieiencies to 90%
- Fully isolated to 1500Vdc
- 1.5" x 2.1" x 0.45" form factor
- -55 to +100°C operation available

Ordering Guide:

Model	Output							Input			Eff. (%)[4]		Options
	Vout (Volts)	Iout (A, max)	Power[1] (Watt)	mVp-p[2]		Regulation		Vin	Range	Iin[3] (mA)			
				Ripple	Noise	Line	Load				min.	typ.	
DH50T12033-10								12	9-20	70	85	87	
DH50T24033-10	3.3	9.0		50	55			24	18-36	60	86	88	
DH50T48033-10	+10	+1.5		60	65			48	36-75	50	85	87	
DH50T24W033-10	-10	-1.5		40	45			24	9-36	60	86	88	
DH50T48W033-10								48	18-75	50	85	87	
DH50T12033-12								12	9-20	70	86	88	
DH50T24033-12	3.3	9.0		50	55			24	18-36	60	87	89	
DH50T48033-12	+12	+1.25	50	65	75	±0.2	±0.5	48	36-75	50	86	88	
DH50T24W033-12	-12	-1.25		50	55			24	9-36	60	87	89	
DH50T48W033-12								48	18-75	50	86	88	
DH50T12033-15								12	9-20	70	87	89	
DH50T24033-15	3.3	9.0		50	55			24	18-36	60	88	90	
DH50T48033-15	+15	+1.0		70	80			48	36-75	50	87	89	
DH50T24W033-15	-15	-1.0		50	55			24	9-36	60	88	90	
DH50T48W033-15								48	18-75	50	87	89	C, MC
DH50T1205-10								12	9-20	70	85	87	
DH50T2405-10	5.0	6.0		40	45			24	18-36	60	86	88	
DH50T4805-10	+10	+1.5		60	65			48	36-75	50	85	87	
DH50T24W05-10	-10	-1.5		40	45			24	9-36	60	86	88	
DH50T48W05-10								48	18-75	50	85	87	
DH50T1205-12								12	9-20	70	86	88	
DH50T2405-12	5.0	6.0		40	45			24	18-36	60	87	89	
DH50T4805-12	+12	+1.25	50	65	75	±0.2	±0.5	48	36-75	50	86	88	
DH50T24W05-12	-12	-1.25		50	55			24	9-36	60	87	89	
DH50T48W05-12								48	18-75	50	86	88	
DH50T1205-15								12	9-20	70	87	89	
DH50T2405-15	5.0	6.0		40	45			24	18-36	60	88	90	
DH50T4805-15	+15	+1.0		70	80			48	36-75	50	87	89	
DH50T24W05-15	-15	-1.0		50	55			24	9-36	60	88	90	
DH50T48W05-15								48	18-75	50	87	89	

[1] Total max. output power.
[2] Max. +V1/+V2,-V3 ripple and noise; measured without external capacitor, 20MHz B.W.
[3] No load input current.
[4] Nominal line voltage and full load.

BLOCK DIAGRAM:



INPUT SPECIFICATIONS:

All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Model No./Parameter	Condition/Description	Min	Nom	Max	Unit
DH50T12033-xx **DH50T1205-xx** (Vin = 9 - 20V)	Input Voltage Range (Continuous)	9	12	20	VDC
	Input Trasient Withstand (100mSec)			30	VDC
	Input Over Voltage Protection		21		VDC
	Output ON (Input Ramping Up)	8.8		9.0	VDC
	Output OFF (Input Ramping Down)	8.2		8.8	VDC
DH50T24033-xx **DH50T2405-xx** (Vin = 18 - 36V)	Input Voltage Continuous	18	24	36	VDC
	Input Trasient Withstand (100mSec)			50	VDC
	Input Over Voltage Protection		37		VDC
	Output ON (Input Ramping Up)	17.8		18	VDC
	Output OFF (Input Ramping Down)	17.2		17.6	VDC
DH50T48033-xx **DH50T4805-xx** (Vin = 36 - 75V)	Input Voltage Continuous	36	48	75	VDC
	Input Trasient Withstand (100mSec)			100	VDC
	Input Over Voltage Protection		77		VDC
	Output ON (Input Ramping Up)	35.6		36	VDC
	Output OFF (Input Ramping Down)	35		35.6	VDC
DH50T24W033-xx **DH50T24W05-xx** (Vin = 9 - 36V)	Input Voltage Continuous	9	24	36	VDC
	Input Trasient Withstand (100mSec)			50	VDC
	Input Over Voltage Protection		37		VDC
	Output ON (Input Ramping Up)	8.8		9.0	VDC
	Output OFF (Input Ramping Down)	8.2		8.8	VDC
DH50T48W033-xx **DH50T48W05-xx** (Vin = 18 - 75V)	Input Voltage Continuous	18	48	75	VDC
	Input Trasient Withstand (100mSec)			100	VDC
	Input Over Voltage Protection		77		VDC
	Output ON (Input Ramping Up)	17.8		18	VDC
	Output OFF (Input Ramping Down)	17.2		17.6	VDC
Input Current	Disabled		8	10	mA
Enable (On/Off) Control	POSITIVE Enable Logic				
	POSITIVE Control [1]	+2.4		+18	VDC
	NEGATIVE Control	0		+1.8	VDC
	Source or Sink current			1.0	mA

[1] Enable Pin Floating = POSITIVE Control.

OUTPUT SPECIFICATIONS:

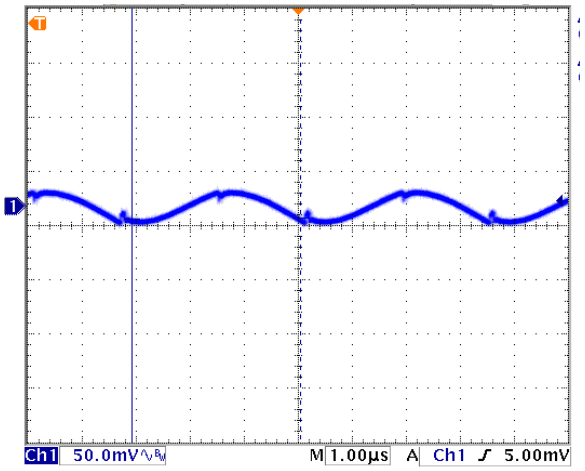
All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Parameter		Condition/Description	Min	Nom	Max	Unit
Output Voltage	3.3V	Vin[1] = Nom, Iout[2] = Min to Max	3.20	3.30	3.40	VDC
	+10V	Vin[1] = Nom, Iout[2] = Min to Max	+9.90	+10.0	+10.10	VDC
	-10V	Balanced Loads on ±10V	-9.90	-10.0	-10.10	VDC
Output Current	3.3V	Baseplate Temperature =< +90°C (Total Output Power 50W max.)	0		9.0	A
	+10V		0	+1.5	+2.0	A
	-10V		0		-1.5	A
Output Trim	3.3V	Trim Up (Trim resistor to trim & (-)3.3V pin)			3.60	V
		Trim Down (Trim resistor to trim & (+)3.3V pin)	3.25			V
Line Regulation	3.3V	Vin = Min to Max, Iout = Max			±0.2	%
	+10V				±0.2	%
	-10V				±0.5	%
Load Regulation	3.3V	Vin = Nom, Iout = Min to Max			±0.5	%
	+10V				±0.5	%
	-10V	Please See Cross Regulation Curves on P. 6			±2.0	%
Ripple & Noise[3]	3.3V	Ripple		30	50	mVp-p
		Spike(20MHz B.W.)		30	55	mVp-p
	+10V	Ripple		40	60	mVp-p
		Spike(20MHz B.W.)		45	65	mVp-p
	-10V	Ripple		25	40	mVp-p
		Spike(20MHz B.W.)		40	45	mVp-p
Transient Response: 75-100-75% step Load	3.3V	Peak Deviation		±50	±70	mV
		Settling Time		100	150	µSec
	+10V	Peak Deviation		±50	±70	mV
		Settling Time		100	120	µSec
Over Voltage Protection	3.3V	Feedback Loop Voltage Clamp		4.3		VDC
	+10V			+13		VDC
Short Circuit Protection		Hiccup Mode Indefinite, Auto Recovery				
Start-Up		Resistive Load		20	25	mSec

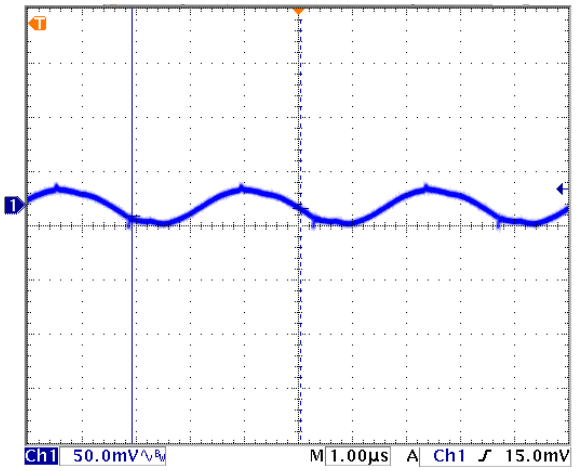
[1] Input Voltage.
[2] Output Current.
[3] Measured without Extenal Capacitor.

Output Ripple & Noise

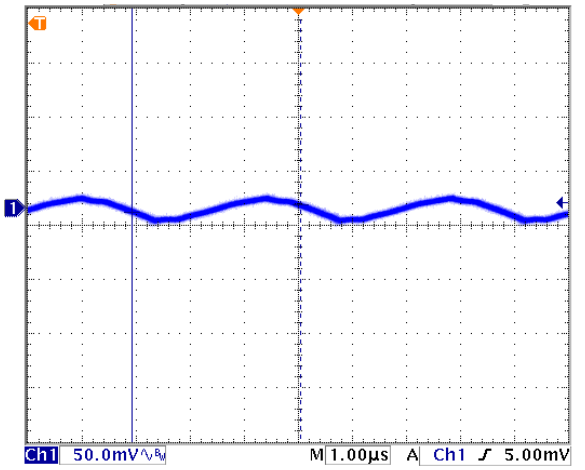
(Measured with no external capacitor, 20MHz B.W.)



3.3V



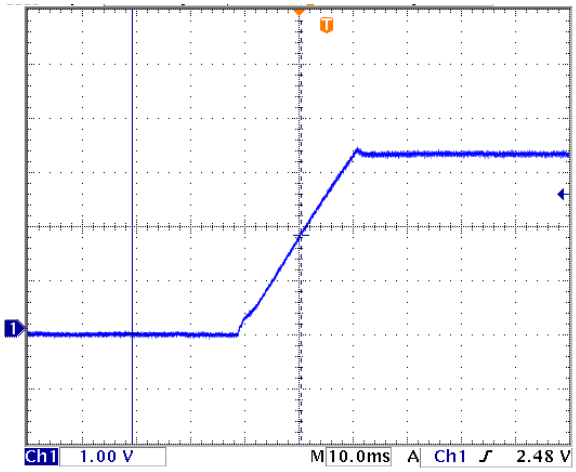
+10V



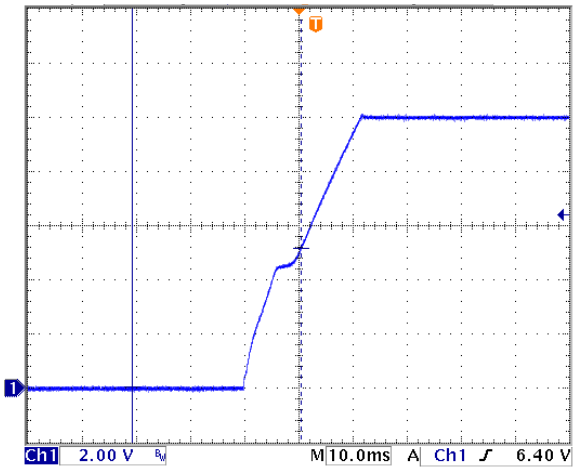
-10V

Start-Up

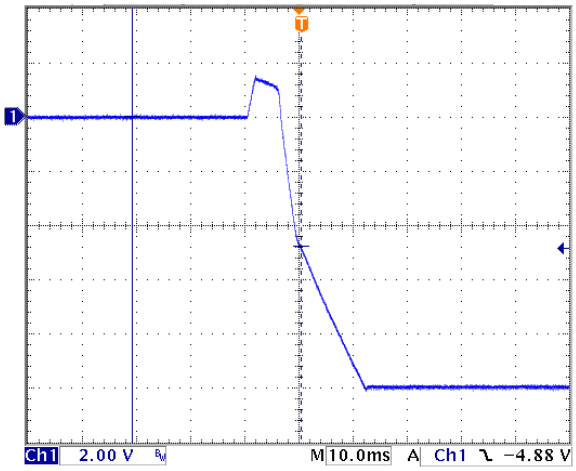
(Resistive, Full Load)



3.3V



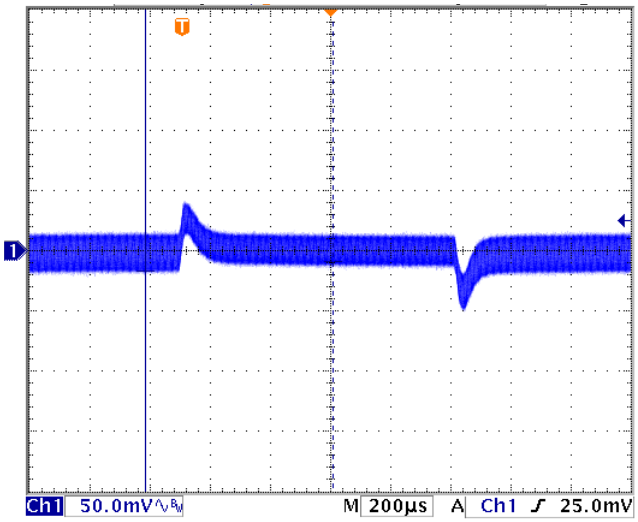
+10V



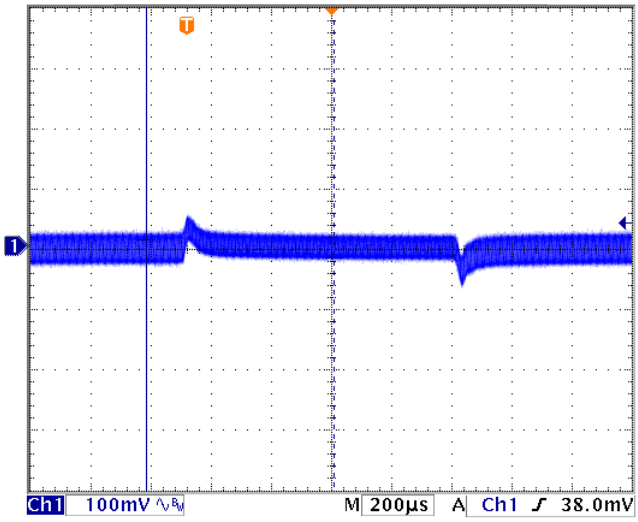
-10V

Output Load Transient

(75% to 100% Step Load change)



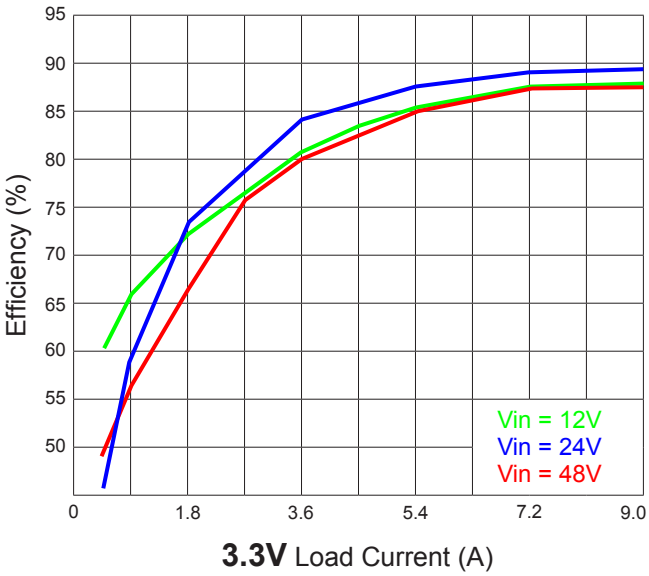
3.3V



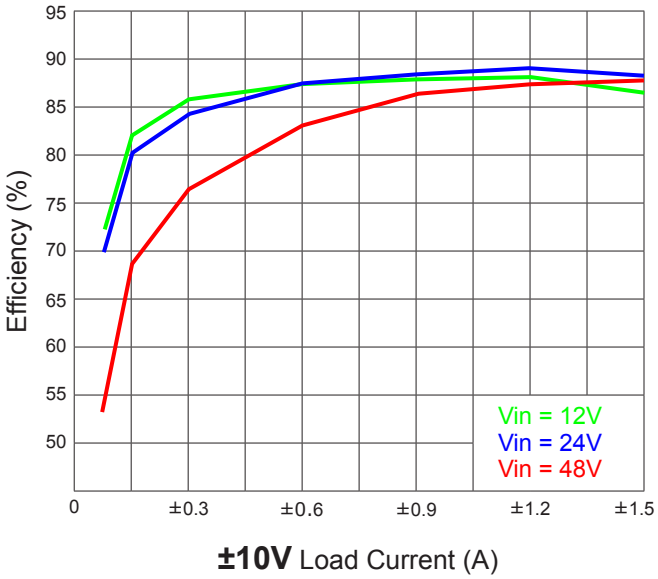
+10V

Efficiency Curves

(Measured @ Baseplate Temp < 50°C)



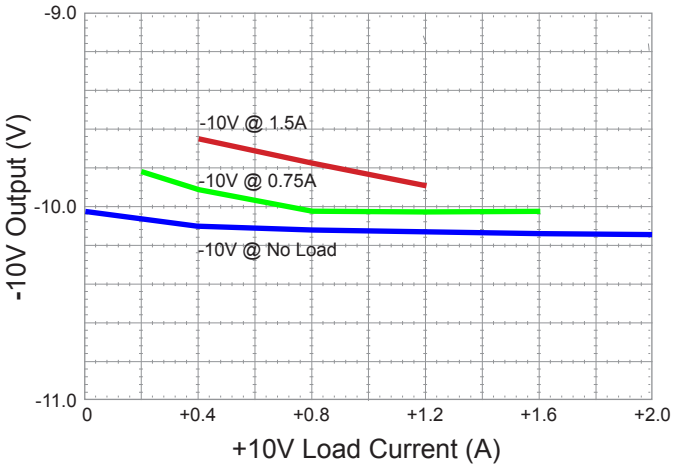
3.3V Load Current (A)



±10V Load Current (A)

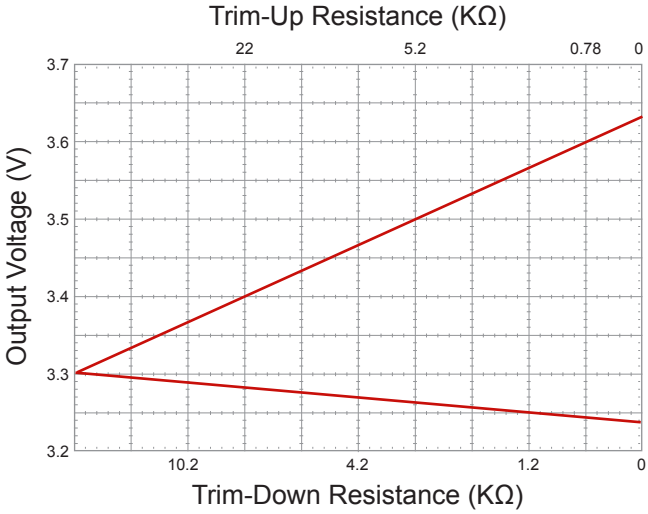
Cross Regulation

(±10V with Unbalanced Loads)



-10V Regulation Curves

Output Voltage Trim



3.3V Trim

OUTPUT SPECIFICATIONS:

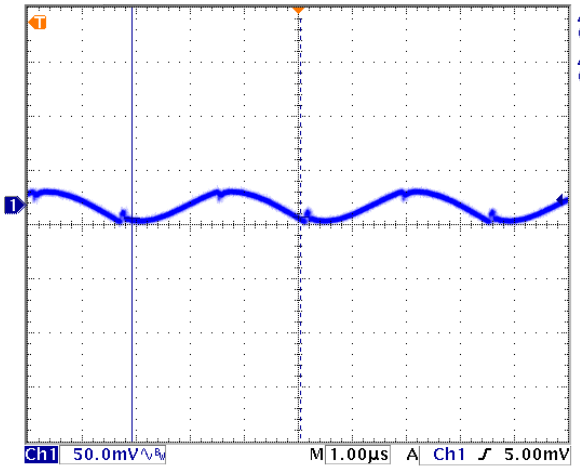
All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Parameter		Condition/Description	Min	Nom	Max	Unit
Output Voltage	3.3V	Vin[1] = Nom, Iout[2] = Min to Max	3.20	3.30	3.40	VDC
	+12V	Vin[1] = Nom, Iout[2] = Min to Max	+11.88	+12.0	+12.12	VDC
	-12V	Balanced Loads on ±10V	-11.88	-12.0	-12.12	VDC
Output Current	3.3V	Baseplate Temperature =< +90°C (Total Output Power 50W max.)	0		9.0	A
	+12V		0	+1.0	+1.5	A
	-12V		0	-1.0	-1.2	A
Output Trim	3.3V	Trim Up (Trim resistor to trim & (-)3.3V pin)			3.60	V
		Trim Down (Trim resistor to trim & (+)3.3V pin)	3.25			V
Line Regulation	3.3V	Vin = Min to Max, Iout = Max			±0.2	%
	+12V				±0.2	%
	-12V				±0.5	%
Load Regulation	3.3V	Vin = Nom, Iout = Min to Max			±0.5	%
	+12V				±0.5	%
	-12V	Please See Cross Regulation Curves on P. 10			±2.0	%
Ripple & Noise[3]	3.3V	Ripple		30	50	mVp-p
		Spike(20MHz B.W.)		30	55	mVp-p
	+12V	Ripple		45	65	mVp-p
		Spike(20MHz B.W.)		50	75	mVp-p
	-12V	Ripple		30	50	mVp-p
		Spike(20MHz B.W.)		35	55	mVp-p
Transient Response: 75-100-75% step Load	3.3V	Peak Deviation		±50	±70	mV
		Settling Time		100	150	µSec
	+12V	Peak Deviation		±50	±70	mV
		Settling Time		100	120	µSec
Over Voltage Protection	3.3V	Feedback Loop Voltage Clamp		4.3		VDC
	+12V			+15		VDC
Short Circuit Protection		Hiccup Mode Indefinite, Auto Recovery				
Start-Up		Resistive Load		20	25	mSec

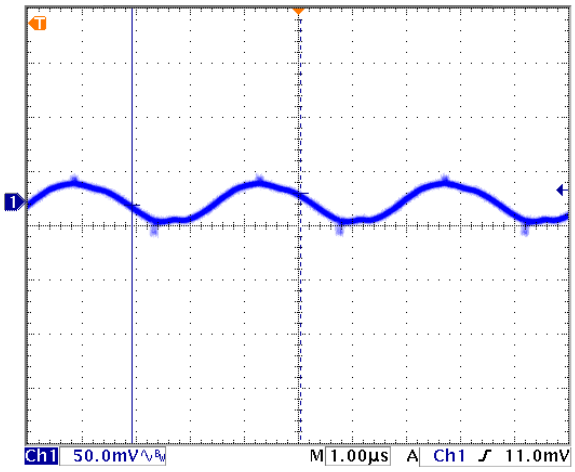
[1] Input Voltage.
[2] Output Current.
[3] Measured without Extenal Capacitor.

Output Ripple & Noise

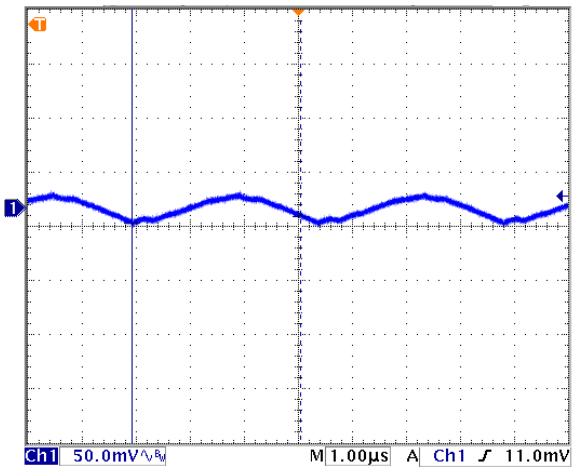
(Measured with no external capacitor, 20MHz B.W.)



3.3V



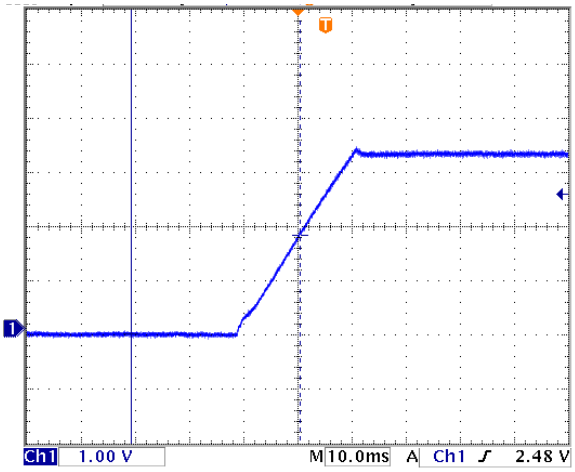
+12V



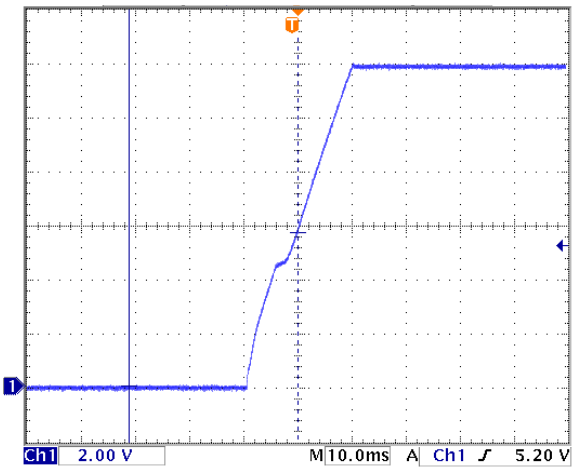
-12V

Start-Up

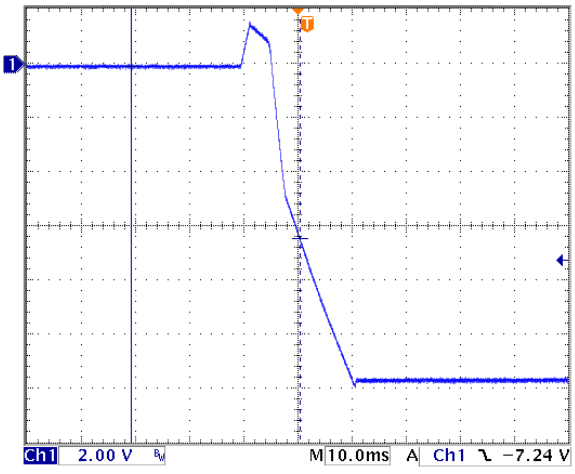
(Resistive, Full Load)



3.3V



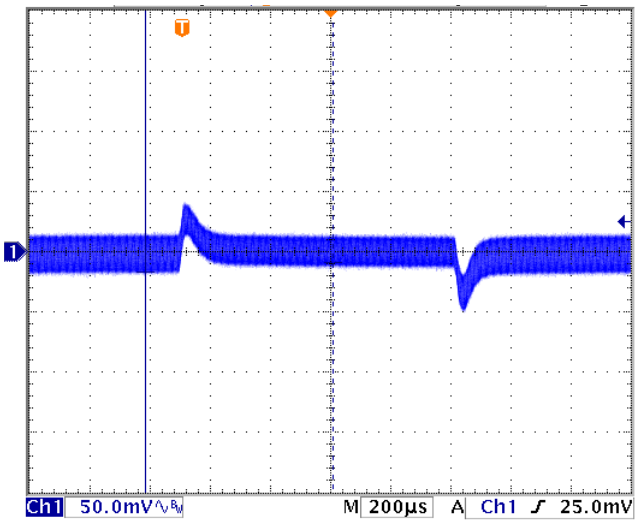
+12V



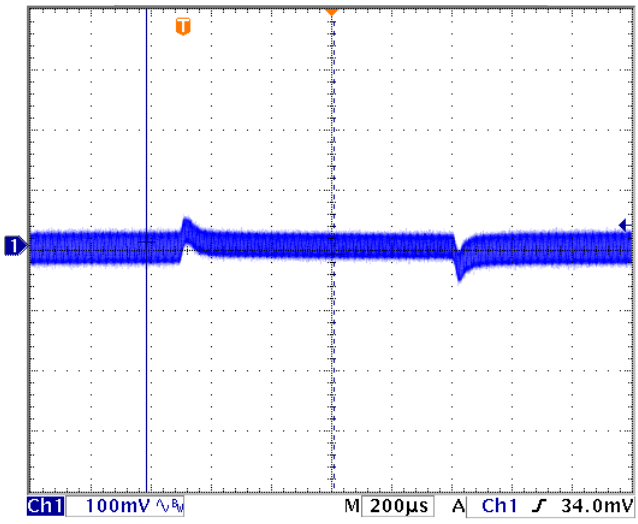
-12V

Output Load Transient

(75% to 100% Step Load change)



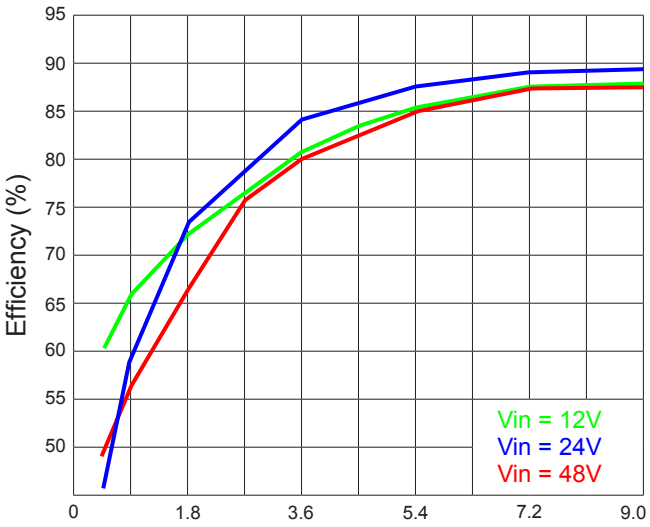
3.3V



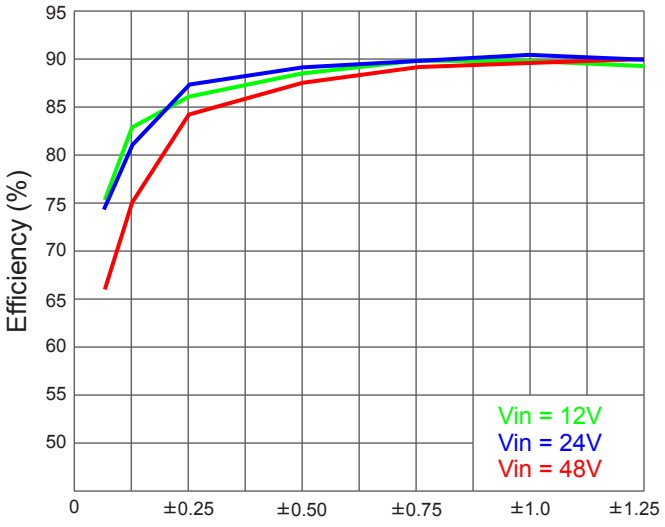
+12V

Efficiency Curves

(Measured @ Baseplate Temp < 50°C)



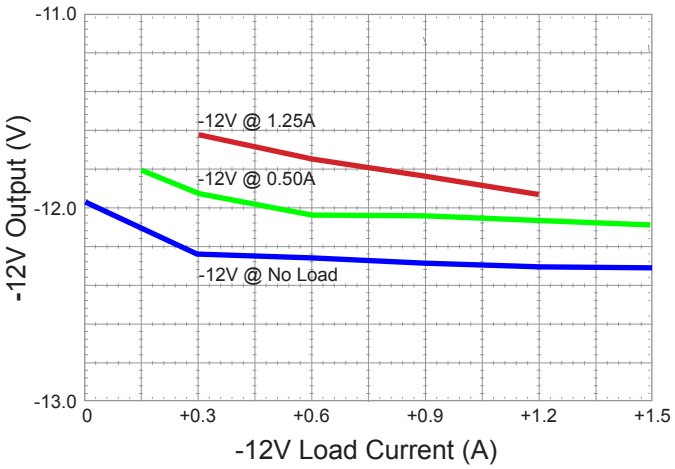
3.3V Load Current (A)



±12V Load Current (A)

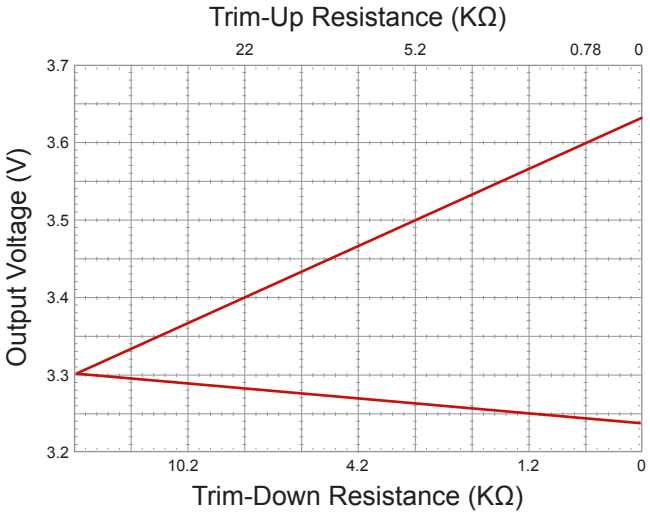
Cross Regulation

(±12V with Unbalanced Loads)



-12V Regulation Curves

Output Voltage Trim



3.3V Trim

OUTPUT SPECIFICATIONS:

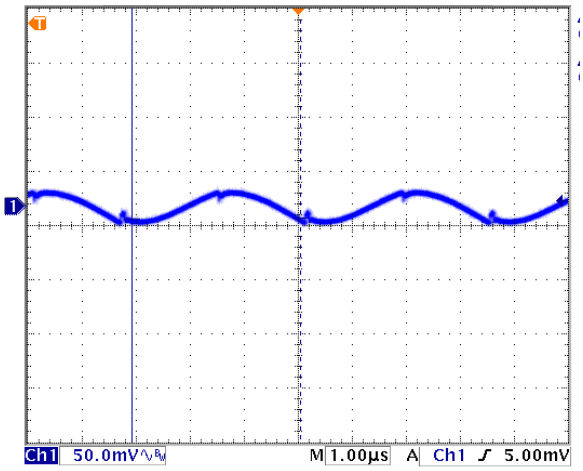
All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Parameter		Condition/Description	Min	Nom	Max	Unit
Output Voltage	3.3V	Vin[1] = Nom, Iout[2] = Min to Max	3.20	3.30	3.40	VDC
	+15V	Vin[1] = Nom, Iout[2] = Min to Max	+14.85	+15.0	+15.15	VDC
	-15V	Balanced Loads on ±10V	-14.85	-15.0	-15.15	VDC
Output Current	3.3V	Baseplate Temperature =< +90°C (Total Output Power 50W max.)	0		9.0	A
	+15V		0	+1.0	+1.2	A
	-15V		0	-1.0	-1.0	A
Output Trim	3.3V	Trim Up (Trim resistor to trim & (-)3.3V pin)			3.60	V
		Trim Down (Trim resistor to trim & (+)3.3V pin)	3.25			V
Line Regulation	3.3V	Vin = Min to Max, Iout = Max			±0.2	%
	+15V				±0.2	%
	-15V				±0.5	%
Load Regulation	3.3V	Vin = Nom, Iout = Min to Max			±0.5	%
	+15V				±0.5	%
	-15V	Please See Cross Regulation Curves on P. 14			±2.0	%
Ripple & Noise[3]	3.3V	Ripple		30	50	mVp-p
		Spike(20MHz B.W.)		30	55	mVp-p
	+15V	Ripple		50	70	mVp-p
		Spike(20MHz B.W.)		55	75	mVp-p
	-15V	Ripple		30	50	mVp-p
		Spike(20MHz B.W.)		35	55	mVp-p
Transient Response: 75-100-75% step Load	3.3V	Peak Deviation		±50	±70	mV
		Settling Time		100	150	µSec
	+15V	Peak Deviation		±50	±70	mV
		Settling Time		100	120	µSec
Over Voltage Protection	3.3V	Feedback Loop Voltage Clamp		4.3		VDC
	+15V			+19		VDC
Short Circuit Protection		Hiccup Mode Indefinite, Auto Recovery				
Start-Up		Resistive Load		20	25	mSec

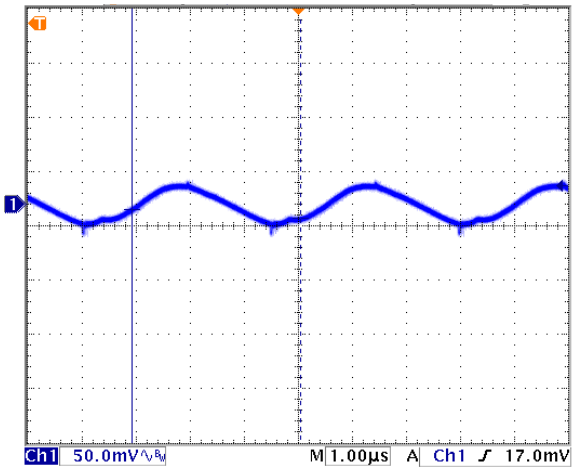
[1] Input Voltage.
[2] Output Current.
[3] Measured without Extenal Capacitor.

Output Ripple & Noise

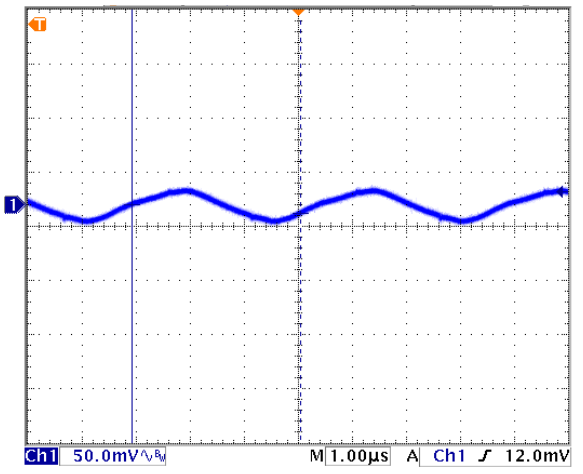
(Measured with no external capacitor, 20MHz B.W.)



3.3V



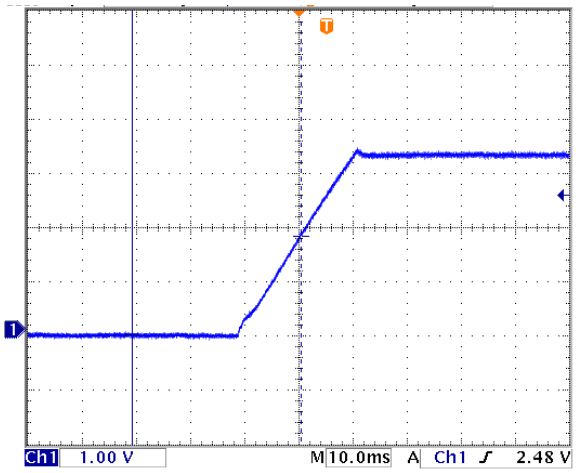
+15V



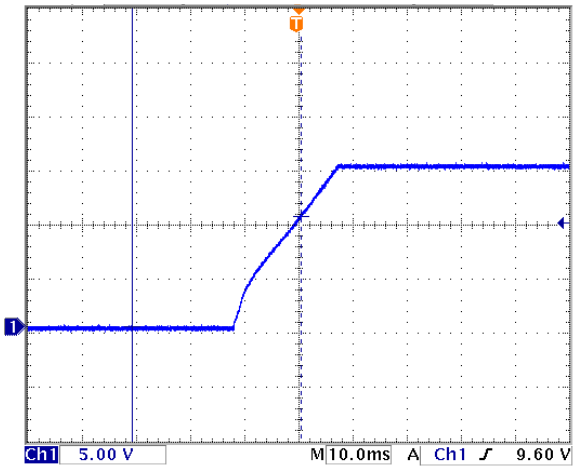
-15V

Start-Up

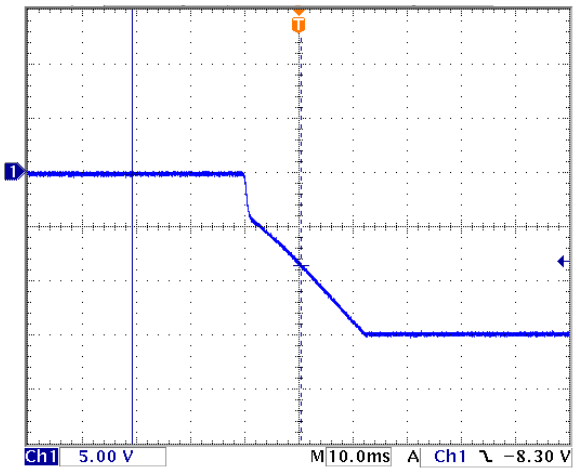
(Resistive, Full Load)



3.3V



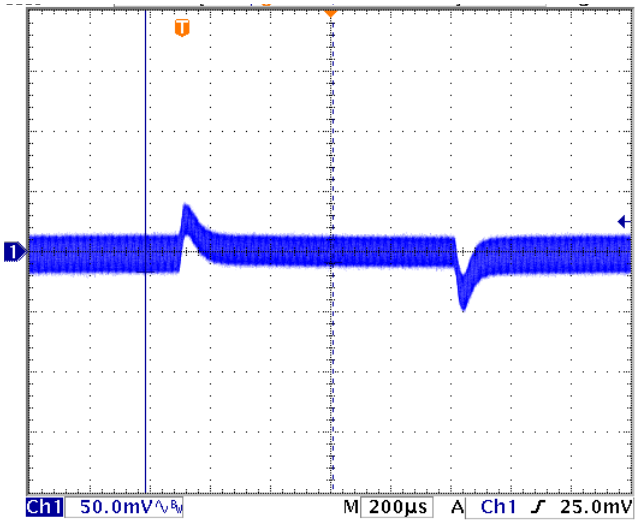
+15V



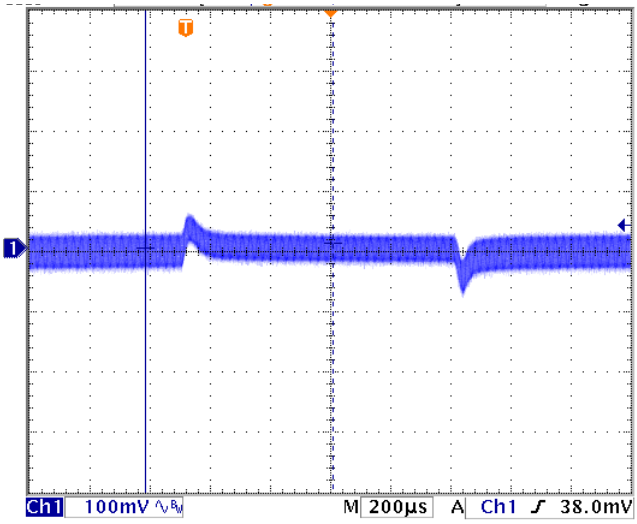
-15V

Output Load Transient

(75% to 100% Step Load change)



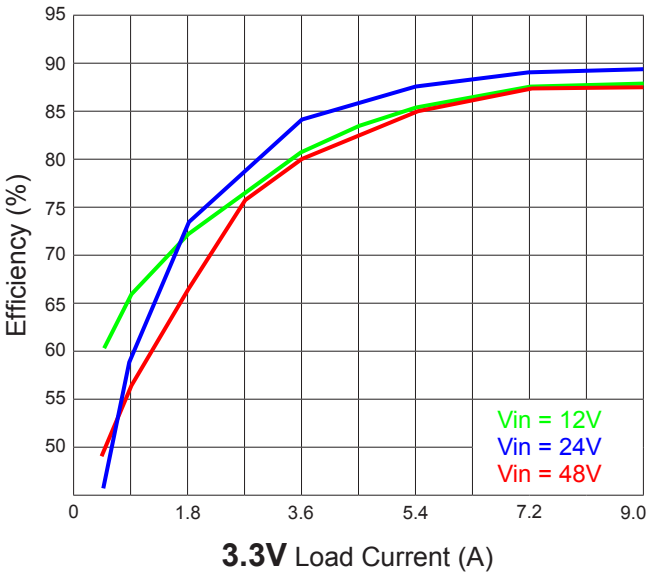
3.3V



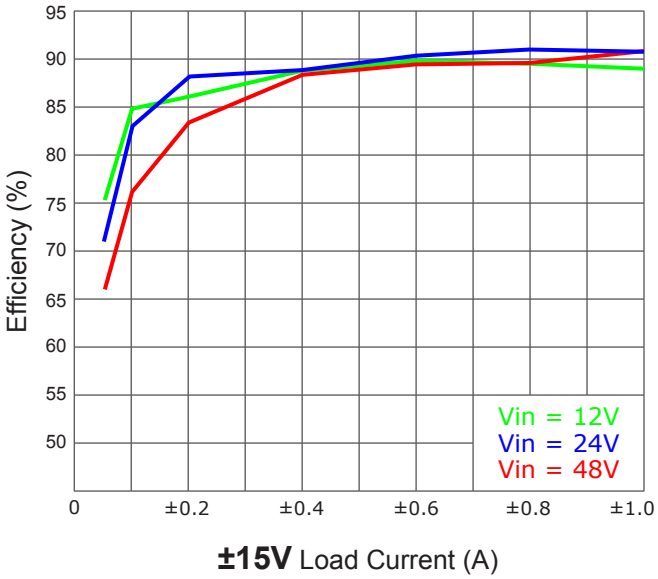
+15V

Efficiency Curves

(Measured @ Baseplate Temp < 50°C)



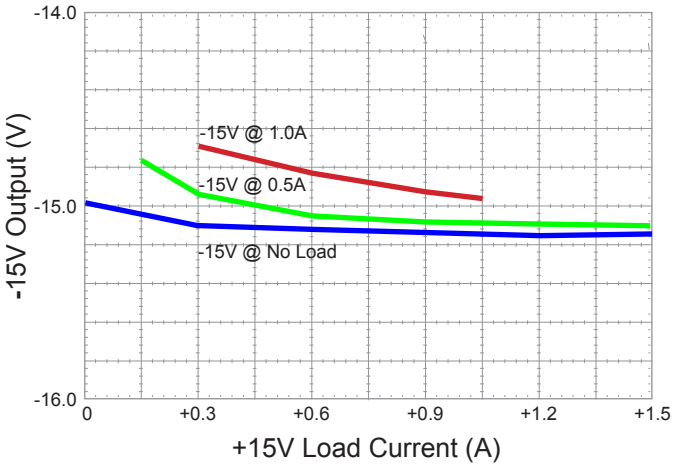
3.3V Load Current (A)



±15V Load Current (A)

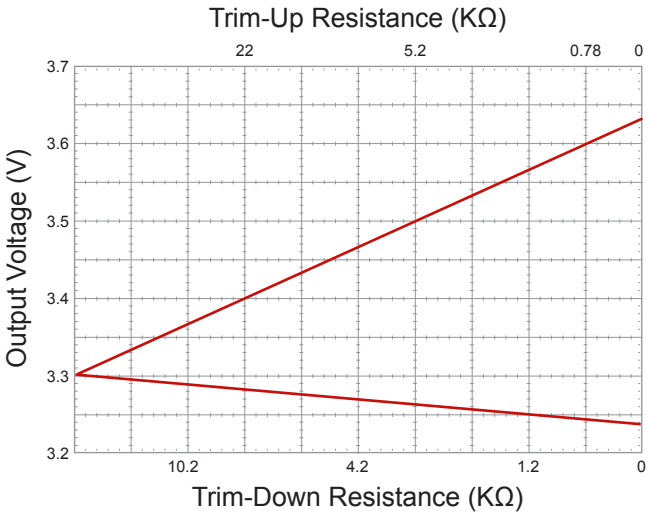
Cross Regulation

(±15V with Unbalanced Loads)



-15V Regulation Curves

Output Voltage Trim



3.3V Trim

OUTPUT SPECIFICATIONS:

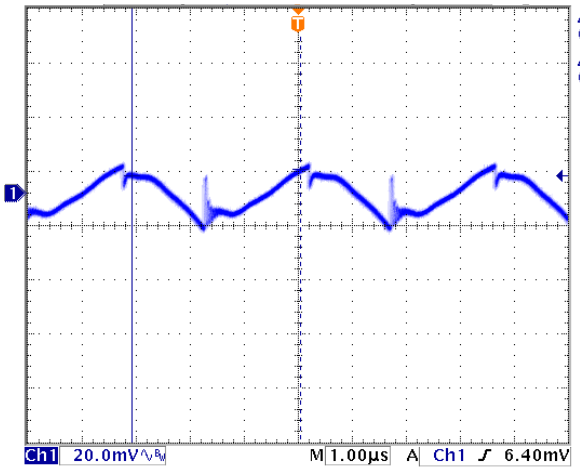
All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Parameter		Condition/Description	Min	Nom	Max	Unit
Output Voltage	5V	Vin[1] = Nom, Iout[2] = Min to Max	4.95	5.0	5.05	VDC
	+10V	Vin[1] = Nom, Iout[2] = Min to Max	+9.90	+10.0	+10.10	VDC
	-10V	Balanced Loads on ±10V	-9.90	-10.0	-10.10	VDC
Output Current	5V	Baseplate Temperature =< +90°C (Total Output Power 50W max.)	0		6.0	A
	+10V		0	+1.5	+2.0	A
	-10V		0		-1.5	A
Output Trim	5V	Trim Up (Trim resistor to trim & (-)5V pin)			5.5	V
		Trim Down (Trim resistor to trim & (+)5V pin)	4.5			V
Line Regulation	5V	Vin = Min to Max, Iout = Max			±0.2	%
	+10V				±0.2	%
	-10V				±0.5	%
Load Regulation	5V	Vin = Nom, Iout = Min to Max			±0.5	%
	+10V				±0.5	%
	-10V	Please See Cross Regulation Curves on p.18			±2.0	%
Ripple & Noise[3]	5V	Ripple		25	40	mVp-p
		Spike(20MHz B.W.)		30	45	mVp-p
	+10V	Ripple		40	60	mVp-p
		Spike(20MHz B.W.)		45	65	mVp-p
	-10V	Ripple		25	40	mVp-p
		Spike(20MHz B.W.)		40	45	mVp-p
Transient Response: 75-100-75% step Load	5V	Peak Deviation		±100	±120	mV
		Settling Time		100	120	µSec
	+10V	Peak Deviation		±50	±70	mV
		Settling Time		100	120	µSec
Over Voltage Protection	5V	Feedback Loop Voltage Clamp		6.5		VDC
	+10V			+13		VDC
Short Circuit Protection		Hiccup Mode Indefinite, Auto Recovery				
Start-Up		Resistive Load		20	25	mSec

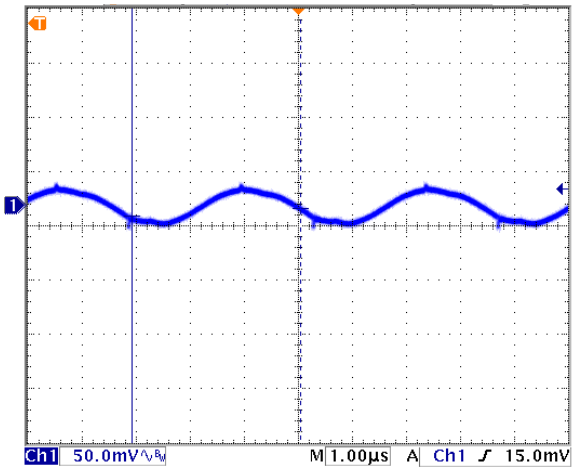
[1] Input Voltage.
[2] Output Current.
[3] Measured without Extenal Capacitor.

Output Ripple & Noise

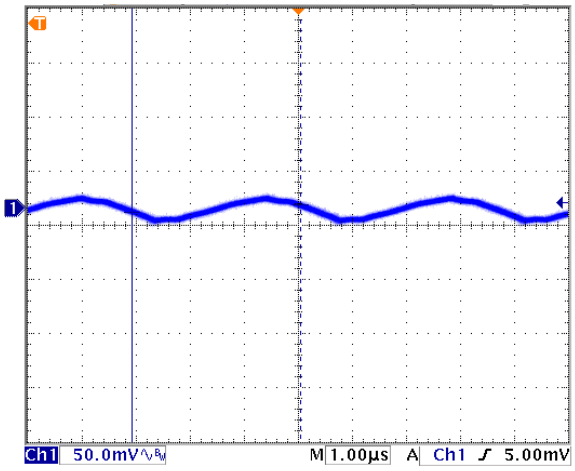
(Measured with no external capacitor, 20MHz B.W.)



5.0V



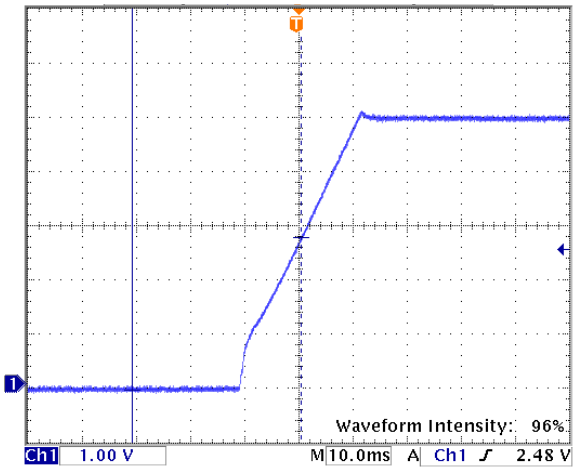
+10V



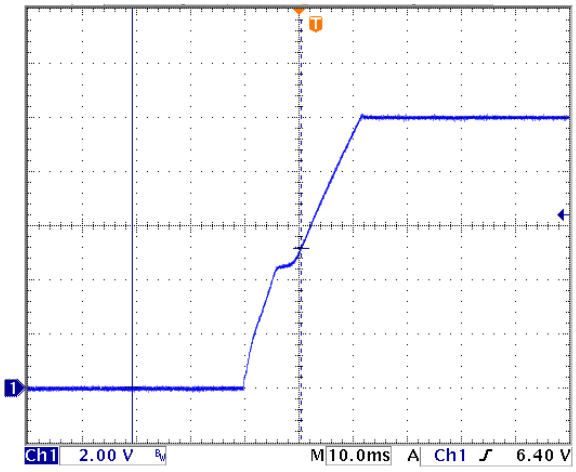
-10V

Start-Up

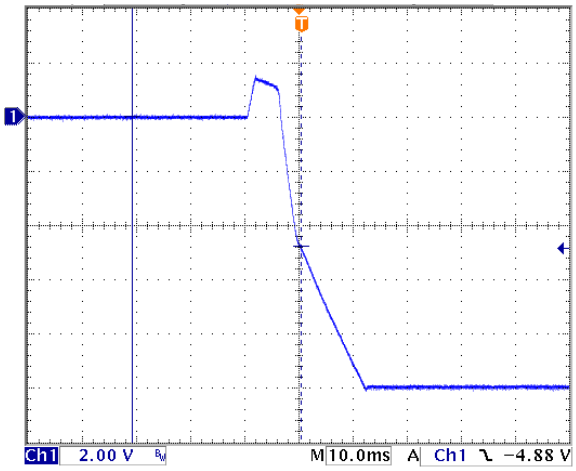
(Resistive, Full Load)



5.0V



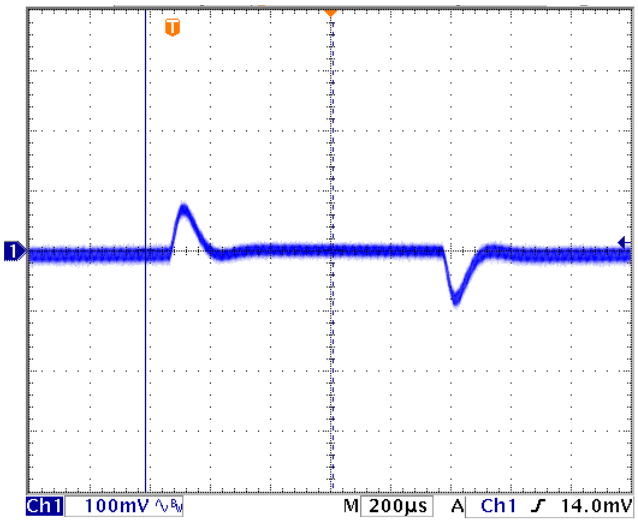
+10V



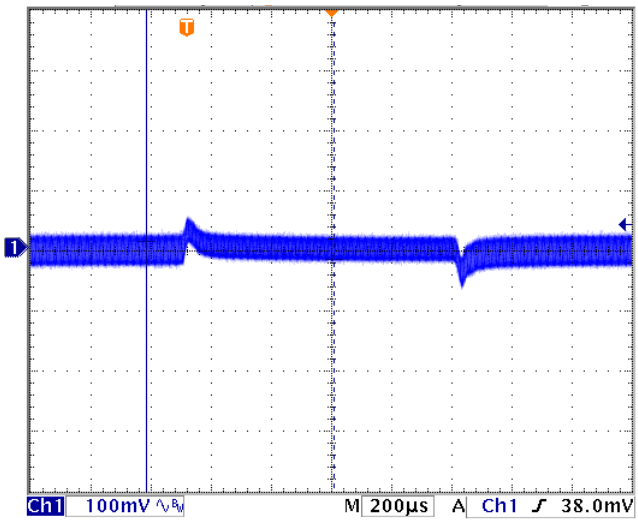
-10V

Output Load Transient

(75% to 100% Step Load change)



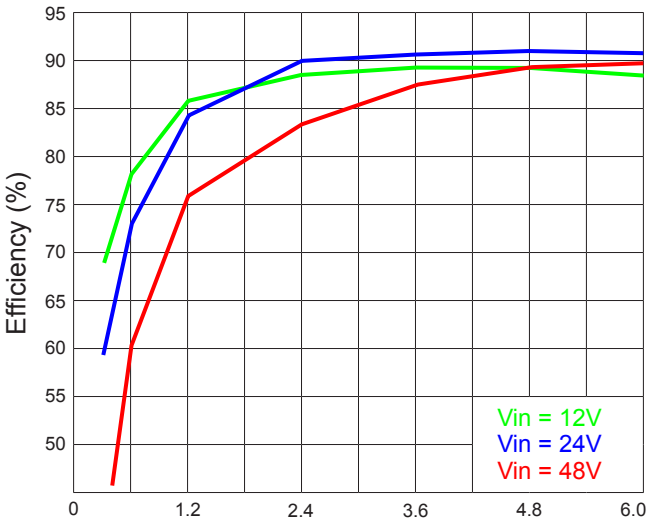
5.0V



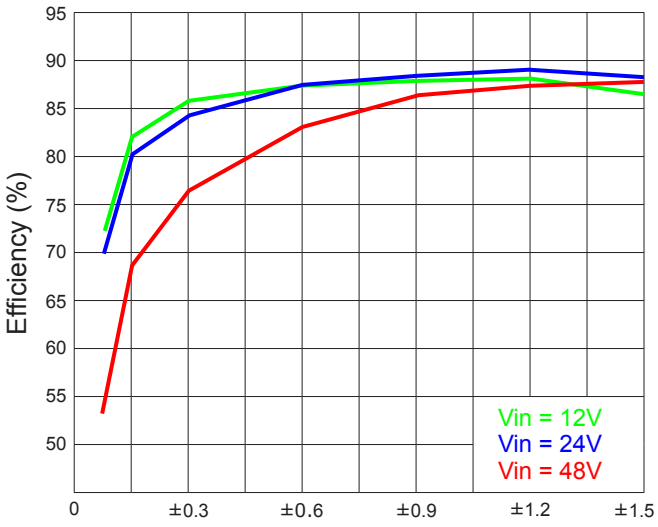
+10V

Efficiency Curves

(Measured @ Baseplate Temp < 50°C)



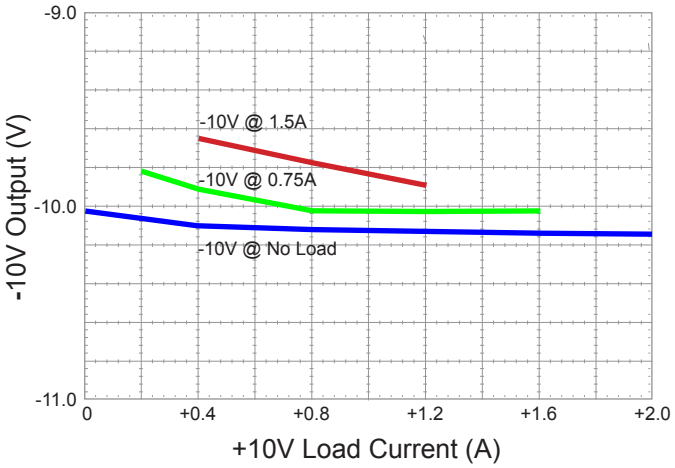
5.0V Load Current (A)



±10V Load Current (A)

Cross Regulation

(±10V with Unbalanced Loads)



-10V Regulation Curves

Output Voltage Trim

5.0V Trim

OUTPUT SPECIFICATIONS:

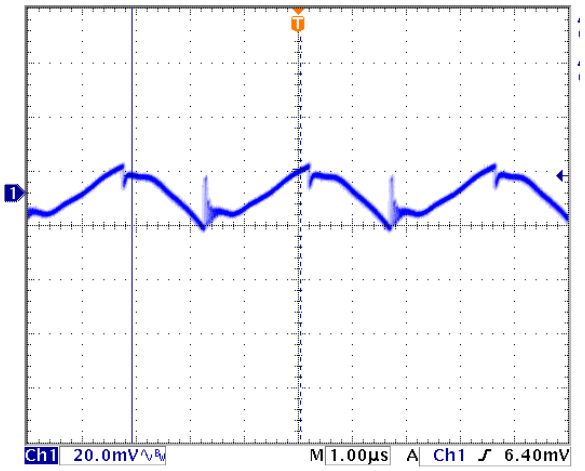
All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Parameter		Condition/Description	Min	Nom	Max	Unit
Output Voltage	5V	Vin[1] = Nom, Iout[2] = Min to Max	4.95	5.0	5.05	VDC
	+12V	Vin[1] = Nom, Iout[2] = Min to Max	+11.88	+12.0	+12.12	VDC
	-12V	Balanced Loads on ±10V	-11.88	-12.0	-12.12	VDC
Output Current	5V	Baseplate Temperature =< +90°C (Total Output Power 50W max.)	0		6.0	A
	+12V		0	+1.0	+1.5	A
	-12V		0	-1.0	-1.2	A
Output Trim	5V	Trim Up (Trim resistor to trim & (-)5V pin)			5.5	V
		Trim Down (Trim resistor to trim & (+)5V pin)	4.5			V
Line Regulation	5V	Vin = Min to Max, Iout = Max			±0.2	%
	+12V				±0.2	%
	-12V				±0.5	%
Load Regulation	5V	Vin = Nom, Iout = Min to Max			±0.5	%
	+12V				±0.5	%
	-12V	Please See Cross Regulation Curves on p.22			±2.0	%
Ripple & Noise[3]	5V	Ripple		25	40	mVp-p
		Spike(20MHz B.W.)		30	45	mVp-p
	+12V	Ripple		45	65	mVp-p
		Spike(20MHz B.W.)		50	75	mVp-p
	-12V	Ripple		30	50	mVp-p
		Spike(20MHz B.W.)		35	55	mVp-p
Transient Response: 75-100-75% step Load	5V	Peak Deviation		±100	±120	mV
		Settling Time		100	120	µSec
	+12V	Peak Deviation		±50	±70	mV
		Settling Time		100	120	µSec
Over Voltage Protection	5V	Feedback Loop Voltage Clamp		6.5		VDC
	+12V			+15		VDC
Short Circuit Protection		Hiccup Mode Indefinite, Auto Recovery				
Start-Up		Resistive Load		20	25	mSec

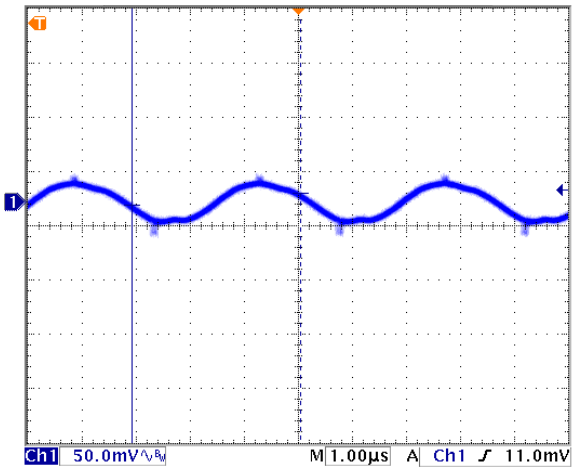
[1] Input Voltage.
[2] Output Current.
[3] Measured without Extenal Capacitor.

Output Ripple & Noise

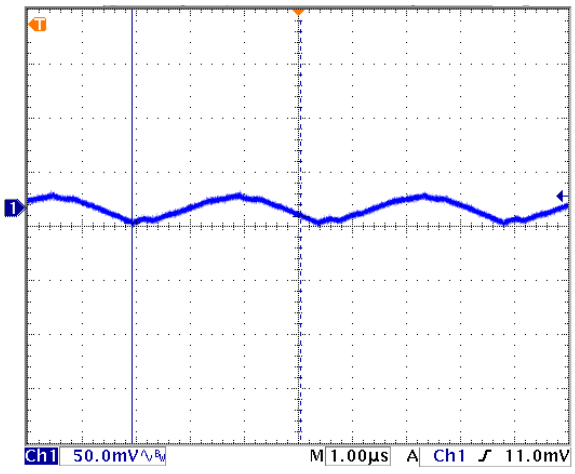
(Measured with no external capacitor, 20MHz B.W.)



5.0V



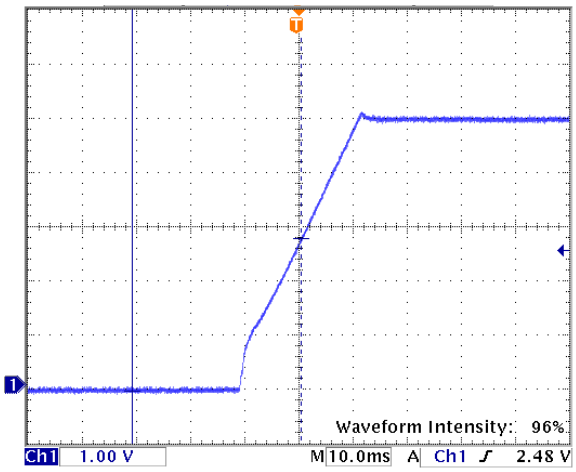
+12V



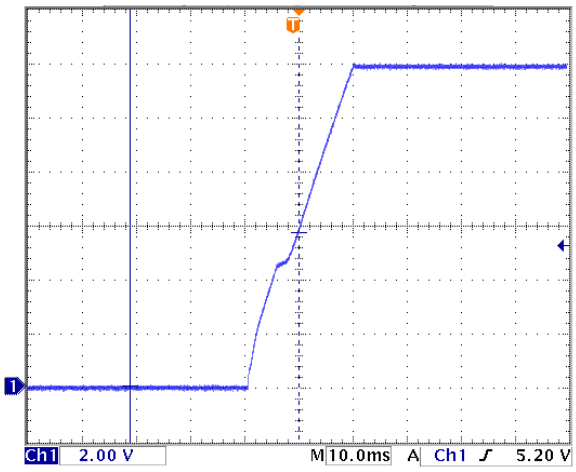
-12V

Start-Up

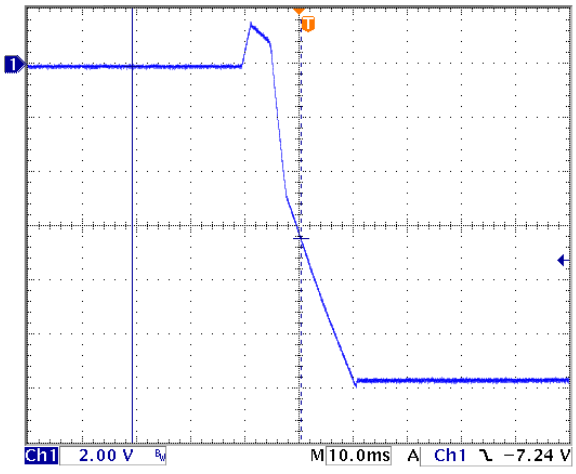
(Resistive, Full Load)



5.0V



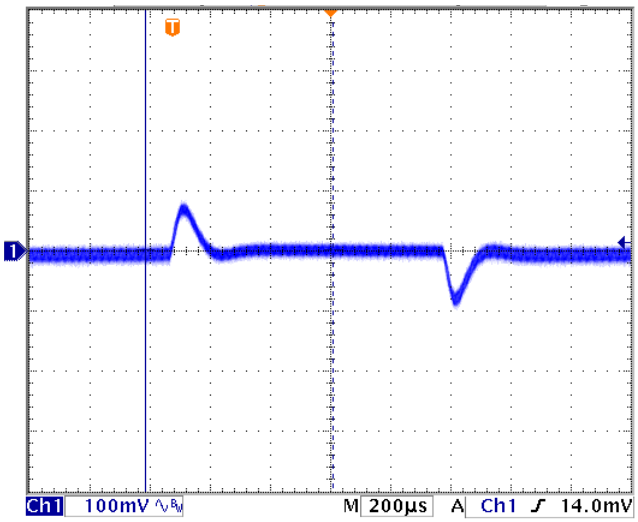
+12V



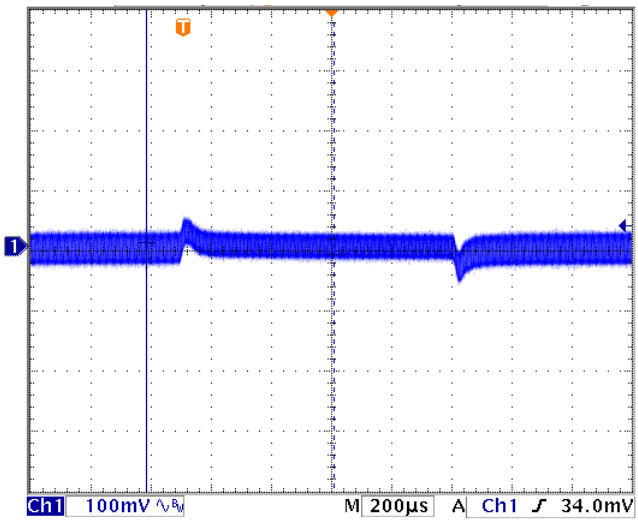
-12V

Output Load Transient

(75% to 100% Step Load change)



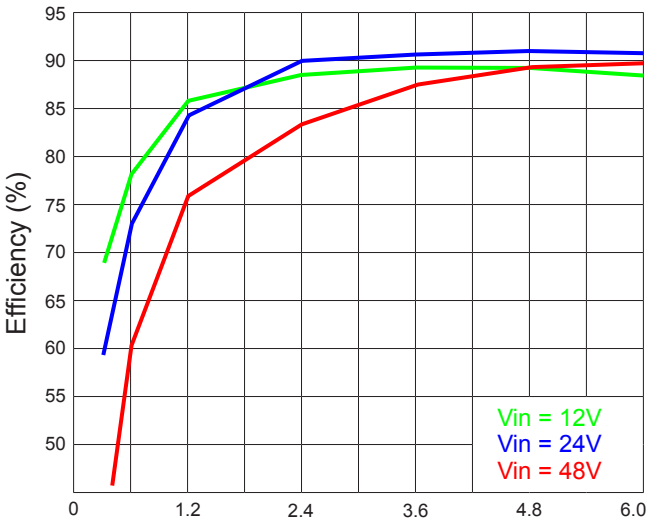
5.0V



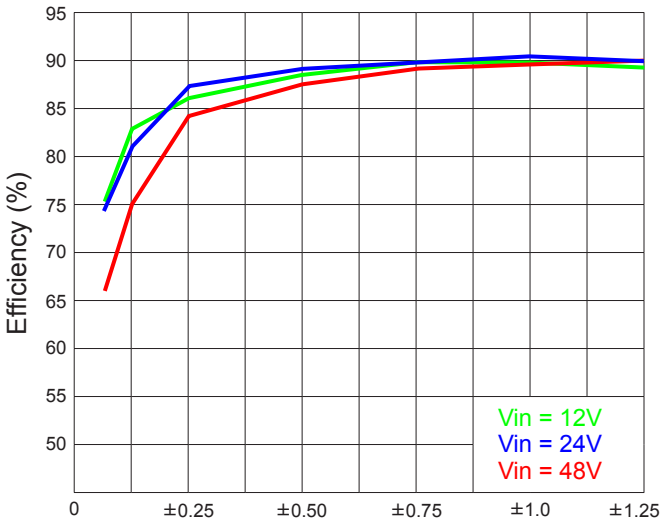
+12V

Efficiency Curves

(Measured @ Baseplate Temp < 50°C)



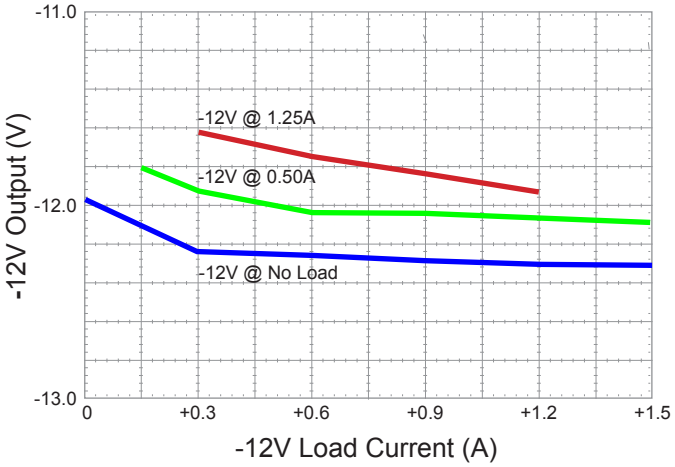
5.0V Load Current (A)



±12V Load Current (A)

Cross Regulation

(±12V with Unbalanced Loads)



-12V Regulation Curves

Output Voltage Trim

5.0V Trim

OUTPUT SPECIFICATIONS:

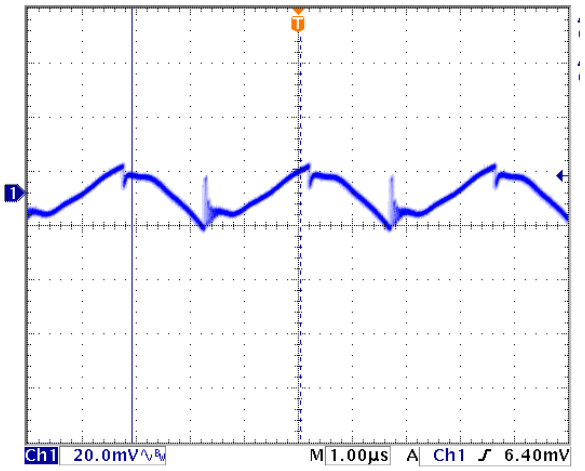
All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Parameter		Condition/Description	Min	Nom	Max	Unit
Output Voltage	5V	Vin[1] = Nom, Iout[2] = Min to Max	4.95	5.0	5.05	VDC
	+15V	Vin[1] = Nom, Iout[2] = Min to Max	+14.85	+15.0	+15.15	VDC
	-15V	Balanced Loads on ±10V	-14.85	-15.0	-15.15	VDC
Output Current	5V	Baseplate Temperature =< +90°C (Total Output Power 50W max.)	0		6.0	A
	+15V		0	+1.0	+1.2	A
	-15V		0	-1.0	-1.0	A
Output Trim	5V	Trim Up (Trim resistor to trim & (-)5V pin)			5.5	V
		Trim Down (Trim resistor to trim & (+)5V pin)	4.5			V
Line Regulation	5V	Vin = Min to Max, Iout = Max			±0.2	%
	+15V				±0.2	%
	-15V				±0.5	%
Load Regulation	5V	Vin = Nom, Iout = Min to Max			±0.5	%
	+15V				±0.5	%
	-15V	Please See Cross Regulation Curves on p.26			±2.0	%
Ripple & Noise[3]	5V	Ripple		25	40	mVp-p
		Spike(20MHz B.W.)		30	45	mVp-p
	+15V	Ripple		50	70	mVp-p
		Spike(20MHz B.W.)		55	75	mVp-p
	-15V	Ripple		30	50	mVp-p
		Spike(20MHz B.W.)		35	55	mVp-p
Transient Response: 75-100-75% step Load	5V	Peak Deviation		±100	±120	mV
		Settling Time		100	120	µSec
	+15V	Peak Deviation		±50	±70	mV
		Settling Time		100	120	µSec
Over Voltage Protection	5V	Feedback Loop Voltage Clamp		6.5		VDC
	+15V			+19		VDC
Short Circuit Protection		Hiccup Mode Indefinite, Auto Recovery				
Start-Up		Resistive Load		20	25	mSec

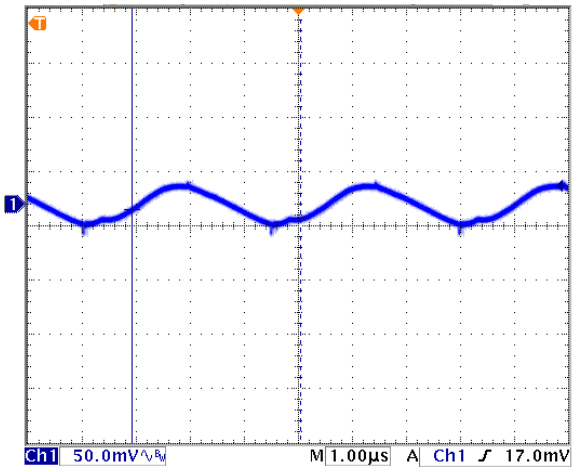
[1] Input Voltage.
[2] Output Current.
[3] Measured without Extenal Capacitor.

Output Ripple & Noise

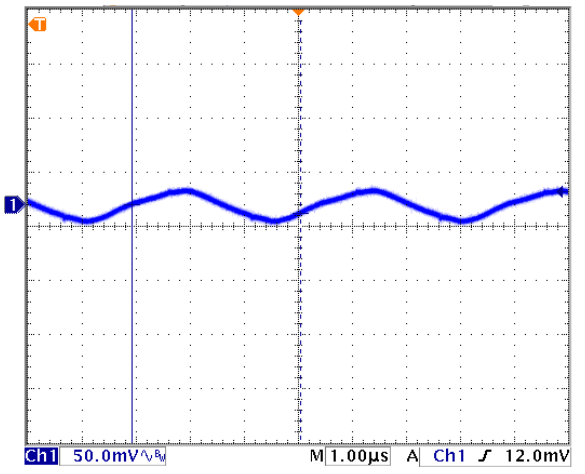
(Measured with no external capacitor, 20MHz B.W.)



5.0V



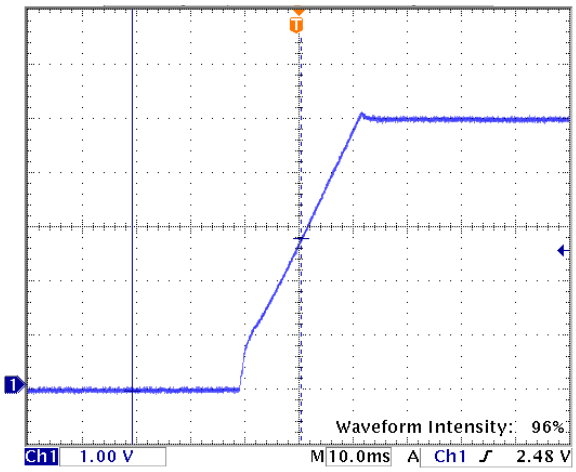
+15V



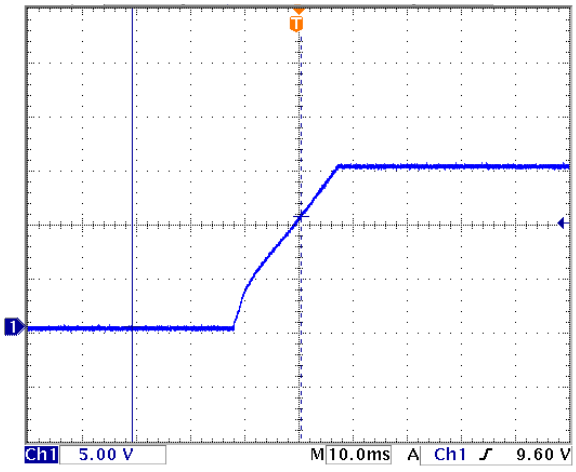
-15V

Start-Up

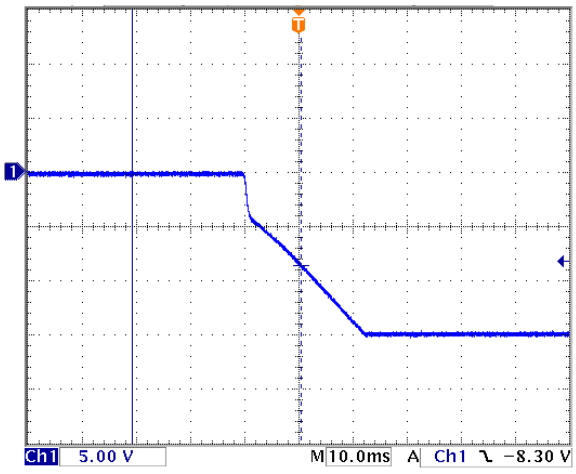
(Resistive, Full Load)



5.0V



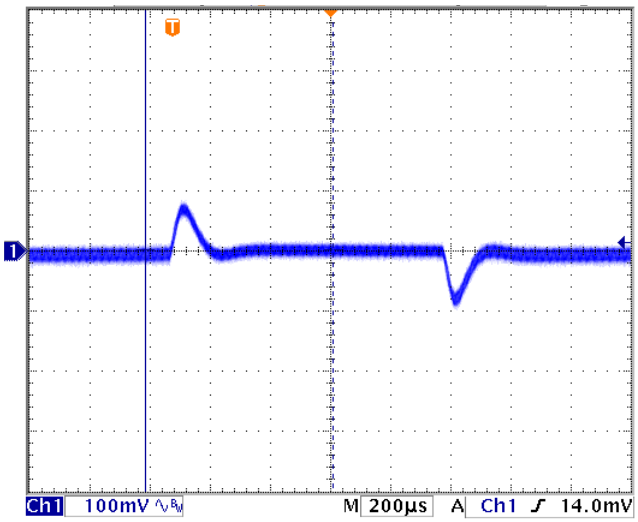
+15V



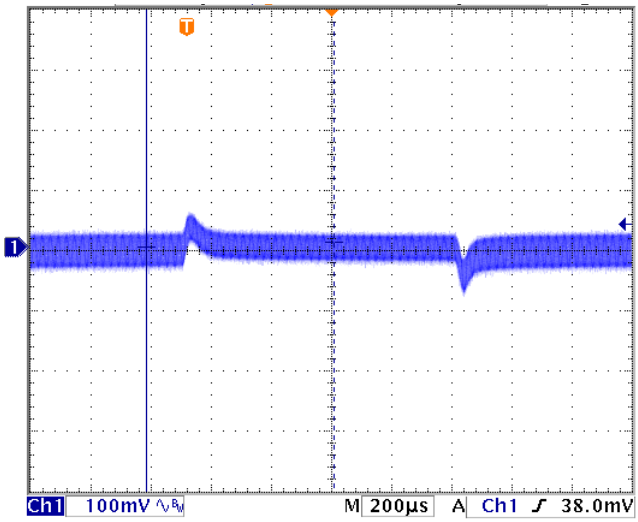
-15V

Output Load Transient

(75% to 100% Step Load change)



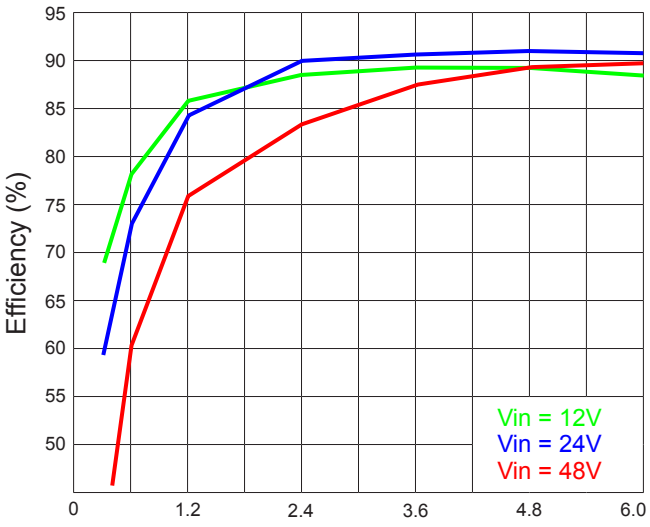
5.0V



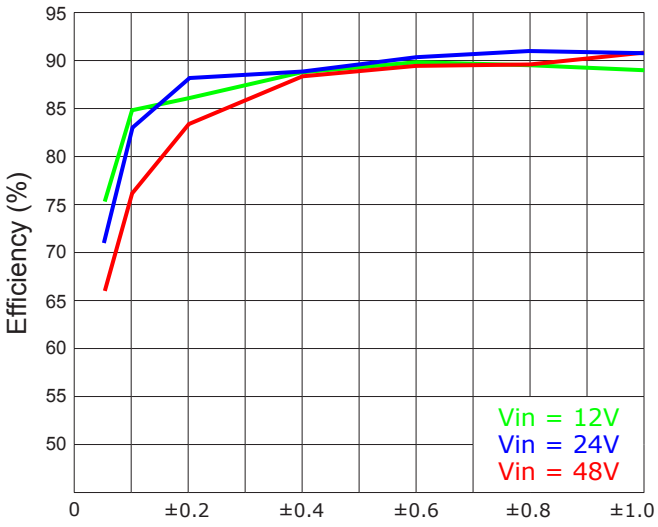
+15V

Efficiency Curves

(Measured @ Baseplate Temp < 50°C)



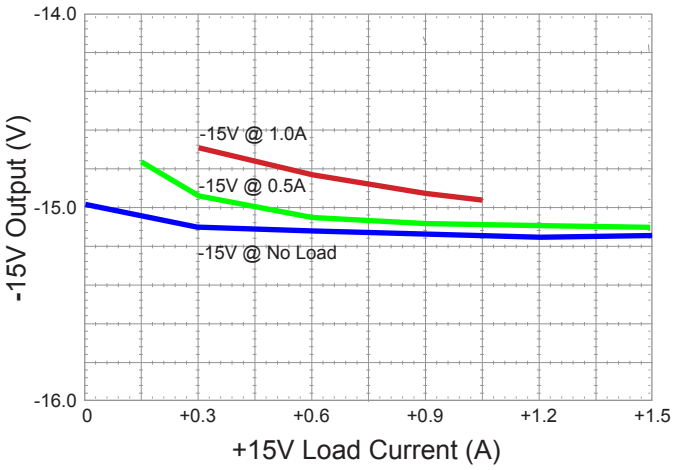
5.0V Load Current (A)



±15V Load Current (A)

Cross Regulation

(±15V with Unbalanced Loads)



-15V Regulation Curves

Output Voltage Trim

5.0V Trim

GENERAL SPECIFICATIONS:

All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Parameter	Condition/Description	Min	Nom	Max	Unit
Operating Frequency	Fixed		300		KHz
Isolation Voltage	Input to Ouput	1500			VDC
	Metal Case to Input/Output	500			VDC
Isolation Resistance	Input to Ouput	10			MΩ
Isolation Capacitance	Input to Ouput		2200		pF
MTBF	Bellcore TR_NWT-000332		1,500		KHrs

[1] Input Voltage.
[2] Output Current.

ENVIROMENTAL SPECIFICATIONS:

All specifications apply over specified input voltage, output load, and temperature range, unless otherwise noted.

Parameter	Condition/Description	Min	Nom	Max	Unit
Temperature Coefficient	−55°C to +100°C Baseplate Temp.		0.02		%/°C
Operating Baseplate Temperature Range	Standard	-40		+85	°C
	Extended	-55		+100	°C
Storage Temperature Range		-55		+125	°C
Thermal Impedance	Per Watt Dissipation [1]		10		°C/W
Over Temperature Protection	Activated @ Baseplate Temperature		+105		°C
	Recovered @ Baseplate Temperature		+95		°C
Humidity	Relative Humidity, Non-Condensing	10		95	%
Shock	(Half-sinewave, 6ms), 3 axes	50			g
Vibration	GR-63-CORE, Section 5.4.2	1			g

[1] Not per Watt Output. Total Dissipation (W) = Total Output Power * (1 / Efficiency - 1).

MECHANICAL SPECIFICATIONS:

Parameter	Condition/Description	Min	Nom	Max	Unit
Dimentions	Please see Drawing on page 29				
Weight	Open Frame		1.3 (35)		Oz (g)
	Encapsulated		2.7 (76)		Oz (g)

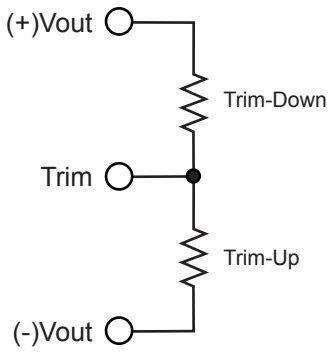
OPTIONS:

DESCRIPTION	OPTION (suffix)	NOTES
Extended Temperature	C	Operating Baseplate Temperature from -55°C to +100°C
Encapsulated	MC	Potted with Thermally Conductive RTV in a Metal Case

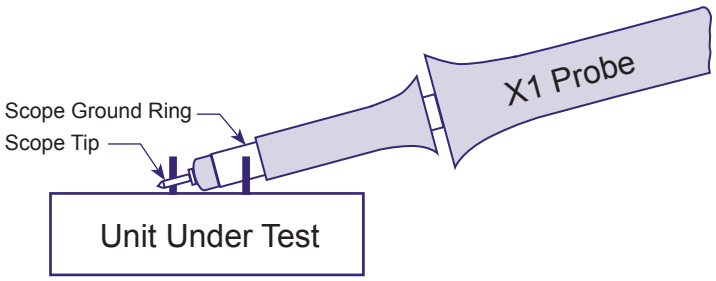
Product Numbering System:

DH	50	D	48W	05	-	10	C	MC
Series	50W Output	Dual Output	18 - 75Vin	5.0Vout	&	10Vout	Extended Temp.	Encapsulated

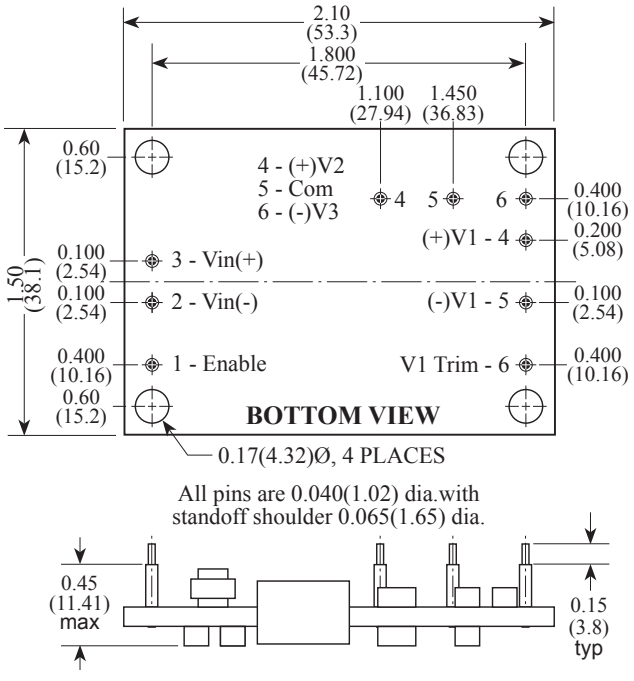
Output Voltage Trim



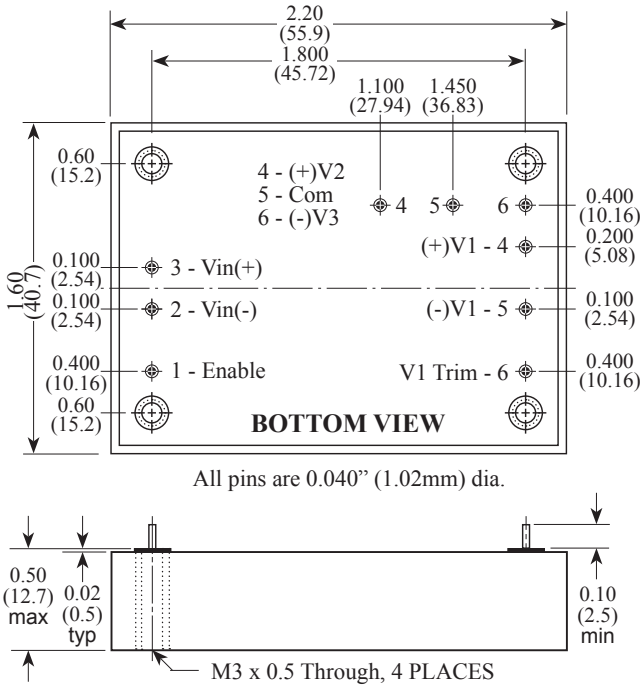
Simplified Ripple & Noise Measurement



Open Frame (Standard, No Suffix)



Encapsulated (Optional, Suffix **MC**)



ALL DIMENSION IN INCHES (mm)
Tolerance .xx = ±0.05
.xxx = ±0.005